



Youngmin Kim, et al.

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Serial No:

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Examiner:

Hsien Ming Lee

Art Unit:

2823

Filed:

05/18/2000

For:

A TUNABLE SIDEWALL SPACER PROCESS FOR CMOS INTEGRATED CIRCUITS

## AMENDMENT UNDER 37 C.F.R. § 1.111

Commissioner For Patents P.O. Box 1450 Alexandria, VA 22313-1450 MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(a)

I hereby certify that the above correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on 1-27-04.

Ann Trent

Dear Sir:

Responsive to the Office Action mailed November 12, 2003, in connection with the above identified application, Applicants respectfully submit the following amendments and remarks.